

# EVLYS LTD. - POWER SEMICONDUCTORS DEVICES - Wholesale and Retail.

## Fast Inverter Thyristor Type FIDT24-200-14

Low switching losses / Low reverse recovery charge  
Distributed amplified gate for high di<sub>T</sub>/dt

Mean on-state current	$I_{TAV}$		200 A		
Repetitive peak off-state voltage	$V_{DRM}$		1000 ÷ 1400 V		
Repetitive peak reverse voltage	$V_{RRM}$				
Turn-off time	$t_q$		6.30, 8.00, 10.0, 12.5 $\mu$ s		
$V_{DRM}, V_{RRM}, V$	1000	1100	1200	1300	1400
Voltage code	10	11	12	13	14
$T_j, ^\circ C$	- 40 ÷ 125				

### MAXIMUM ALLOWABLE RATINGS

Symbols and parameters		Units	Values	Test conditions
<b>ON-STATE</b>				
$I_{TAV}$	Mean on-state current	A	200 280	$T_c = 83^\circ C$ ; Double side cooled; $T_c = 55^\circ C$ ; Double side cooled; 180° half-sine wave; 50 Hz
$I_{TRMS}$	RMS on-state current	A	314	$T_c = 83^\circ C$ ; Double side cooled; 180° half-sine wave; 50 Hz
$I_{TSM}$	Surge on-state current	kA	3.5 4.0	$T_j = T_{jmax}$ $T_j = 25^\circ C$ 180° half-sine wave; $t_p = 10$ ms; single pulse; $V_D = V_R = 0$ V; Gate pulse: $I_G = I_{FGM}$ ; $V_G = 20$ V; $t_{GP} = 50$ $\mu$ s; $di_G/dt = 1$ A/ $\mu$ s
			3.5 4.0	$T_j = T_{jmax}$ $T_j = 25^\circ C$ 180° half-sine wave; $t_p = 8.3$ ms; single pulse; $V_D = V_R = 0$ V; Gate pulse: $I_G = I_{FGM}$ ; $V_G = 20$ V; $t_{GP} = 50$ $\mu$ s; $di_G/dt = 1$ A/ $\mu$ s
$I^2t$	Safety factor	$A^2s \cdot 10^3$	60 80	$T_j = T_{jmax}$ $T_j = 25^\circ C$ 180° half-sine wave; $t_p = 10$ ms; single pulse; $V_D = V_R = 0$ V; Gate pulse: $I_G = I_{FGM}$ ; $V_G = 20$ V; $t_{GP} = 50$ $\mu$ s; $di_G/dt = 1$ A/ $\mu$ s
			50 60	$T_j = T_{jmax}$ $T_j = 25^\circ C$ 180° half-sine wave; $t_p = 8.3$ ms; single pulse; $V_D = V_R = 0$ V; Gate pulse: $I_G = I_{FGM}$ ; $V_G = 20$ V; $t_{GP} = 50$ $\mu$ s; $di_G/dt = 1$ A/ $\mu$ s
<b>BLOCKING</b>				
$V_{DRM}, V_{RRM}$	Repetitive peak off-state and Repetitive peak reverse voltages	V	1000÷1400	$T_{jmin} < T_j < T_{jmax}$ ; 180° half-sine wave; 50 Hz; Gate open
$V_{DSM}, V_{RSM}$	Non-repetitive peak off-state and Non-repetitive peak reverse voltages	V	1100÷1500	$T_{jmin} < T_j < T_{jmax}$ ; 180° half-sine wave; single pulse; Gate open
$V_D, V_R$	Direct off-state and Direct reverse voltages	V	$0.6 \cdot V_{DRM}$ $0.6 \cdot V_{RRM}$	$T_j = T_{jmax}$ ; Gate open

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<b>TRIGGERING</b>				
$I_{FGM}$	Peak forward gate current	A	5	$T_j = T_{j\max}$
$V_{RGM}$	Peak reverse gate voltage	V	5	
$P_G$	Gate power dissipation	W	3	$T_j = T_{j\max}$ for DC gate current
<b>SWITCHING</b>				
$(di_T/dt)_{crit}$	Critical rate of rise of on-state current non-repetitive (f=1 Hz)	A/ $\mu$ S	1000	$T_j = T_{j\max}$ ; $V_D = 0.67 \cdot V_{DRM}$ ; $I_{TM} = 2 I_{TAV}$ ; Gate pulse: $I_G = I_{FGM}$ ; $V_G = 20$ V; $t_{GP} = 50$ $\mu$ S; $di_G/dt = 2$ A/ $\mu$ S
<b>THERMAL</b>				
$T_{stg}$	Storage temperature	$^{\circ}$ C	-60 ÷ 50	
$T_j$	Operating junction temperature	$^{\circ}$ C	-60 ÷ 125	
<b>MECHANICAL</b>				
F	Mounting force	kN	5.0 ÷ 7.0	
a	Acceleration	m/s <sup>2</sup>	50	Device clamped

## CHARACTERISTICS

Symbols and parameters		Units	Values	Conditions	
<b>ON-STATE</b>					
$V_{TM}$	Peak on-state voltage, max	V	3.30	$T_j = 25$ $^{\circ}$ C; $I_{TM} = 628$ A	
$V_{T(TO)}$	On-state threshold voltage, max	V	1.40	$T_j = T_{j\max}$ ;	
$r_T$	On-state slope resistance, max	m $\Omega$	3.20	$0.5 \pi I_{TAV} < I_T < 1.5 \pi I_{TAV}$	
$I_H$	Holding current, max	mA	200	$T_j = 25$ $^{\circ}$ C; $V_D = 12$ V; Gate open	
<b>BLOCKING</b>					
$I_{DRM}$ , $I_{RRM}$	Repetitive peak off-state and Repetitive peak reverse currents, max	mA	50	$T_j = T_{j\max}$ ; $V_D = V_{DRM}$ ; $V_R = V_{RRM}$	
$(dv_D/dt)_{crit}$	Critical rate of rise of off-state voltage <sup>1)</sup> , min	V/ $\mu$ S	200, 320, 500, 1000	$T_j = T_{j\max}$ ; $V_D = 0.67 \cdot V_{DRM}$ ; Gate open	
<b>TRIGGERING</b>					
$V_{GT}$	Gate trigger direct voltage, max	V	4.00 2.50 2.00	$T_j = T_{j\min}$ $T_j = 25$ $^{\circ}$ C $T_j = T_{j\max}$	$V_D = 12$ V; $I_D = 3$ A; Direct gate current
$I_{GT}$	Gate trigger direct current, max	mA	500 300 200	$T_j = T_{j\min}$ $T_j = 25$ $^{\circ}$ C $T_j = T_{j\max}$	
$V_{GD}$	Gate non-trigger direct voltage, min	V	0.25	$T_j = T_{j\max}$ ; $V_D = 0.67 \cdot V_{DRM}$ ;	
$I_{GD}$	Gate non-trigger direct current, min	mA	10.00	Direct gate current	
<b>SWITCHING</b>					
$t_{gd}$	Delay time, max	$\mu$ S	0.58	$T_j = 25$ $^{\circ}$ C; $V_D = 600$ V; $I_{TM} = I_{TAV}$ ; $di/dt = 200$ A/ $\mu$ S;	
$t_{gt}$	Turn-on time <sup>2)</sup>	$\mu$ S	1.25, 1.60, 2.00, 2.50	Gate pulse: $I_G = I_{FGM}$ ; $V_G = 20$ V; $t_{GP} = 50$ $\mu$ S; $di_G/dt = 2$ A/ $\mu$ S	
$t_q$	Turn-off time <sup>3)</sup> , max	$\mu$ S	6.30, 8.00, 10.0, 12.5 8.00, 10.0, 12.5, 16.0	$dv_D/dt = 50$ V/ $\mu$ S; $dv_D/dt = 200$ V/ $\mu$ S;	$T_j = T_{j\max}$ ; $I_{TM} = I_{TAV}$ ; $di_R/dt = -10$ A/ $\mu$ S; $V_R = 100$ V; $V_D = 0.67 V_{DRM}$
$Q_{rr}$	Total recovered charge, max	$\mu$ C	80.0	$T_j = T_{j\max}$ ; $I_{TM} = 200$ A;	
$t_{rr}$	Reverse recovery time, typ	$\mu$ S	2.1	$di_R/dt = -50$ A/ $\mu$ S;	
$I_{rrM}$	Peak reverse recovery current, max	A	75	$V_R = 100$ V	

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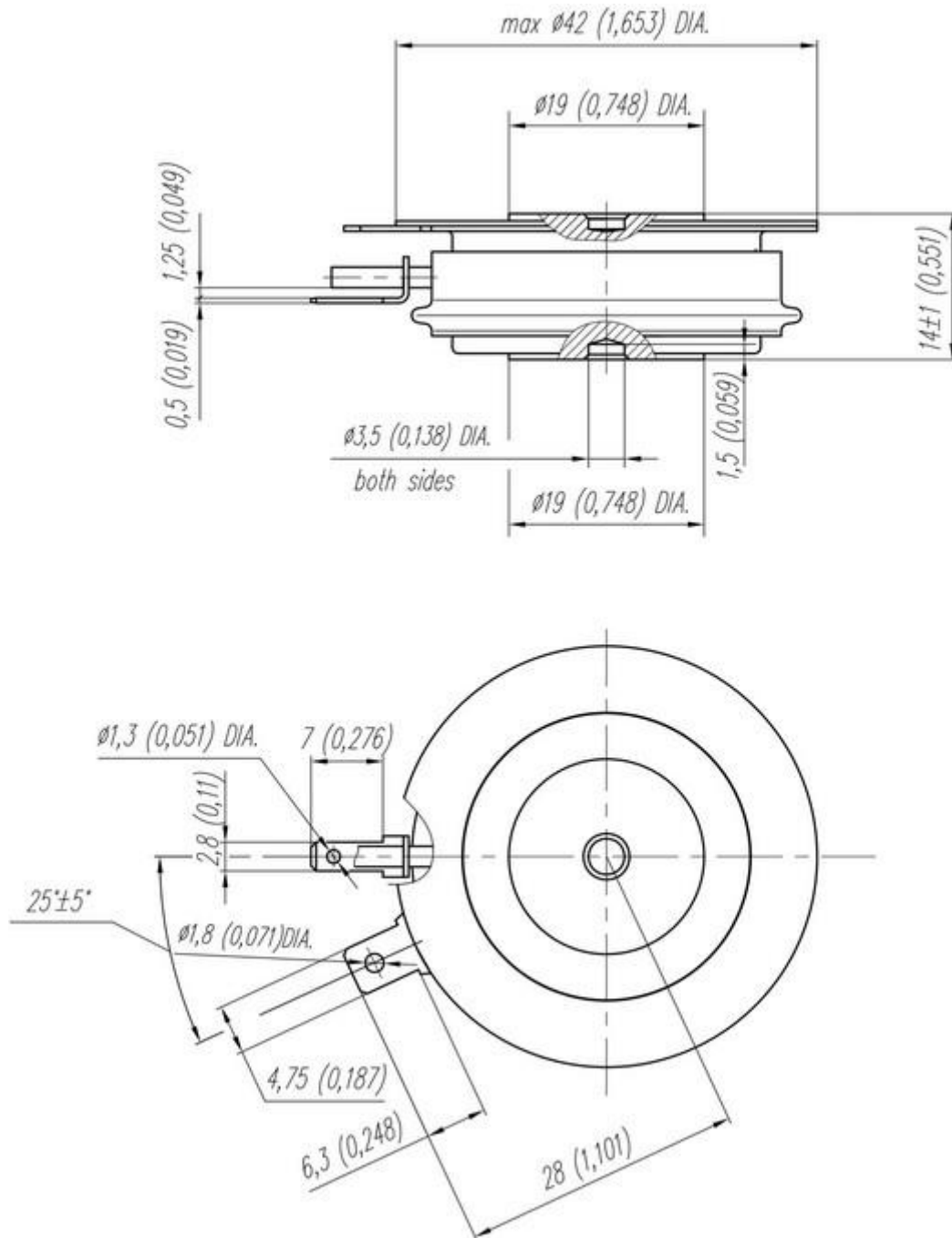
<b>THERMAL</b>					
$R_{thjc}$	Thermal resistance, junction to case, max	°C/W	0.0700	Direct current	Double side cooled
$R_{thjc-A}$			0.1570		Anode side cooled
$R_{thjc-K}$			0.1260		Cathode side cooled
$R_{thck}$	Thermal resistance, case to heatsink, max	°C/W	0.0100	Direct current	
<b>MECHANICAL</b>					
w	Weight, max	g	55		
$D_s$	Surface creepage distance	mm (inch)	7.94 (0.313)		
$D_a$	Air strike distance	mm (inch)	5.00 (0.197)		

<b>PART NUMBERING GUIDE</b>								<b>NOTES</b>						
FIDT	24	200	14	7	9	5		1) Critical rate of rise of off-state voltage						
1	2	3	4	5	6	7		Symbol of group	4	5	6	7		
1. FIDT — Fast Inverter Disc Thyristor with Distributed Amplified Gate								$(dv_D/dt)_{crit}, V/\mu s$	200	320	500	1000		
2. Element Diameter								2) Turn-on time						
3. Mean on-state current, A								Symbol of group	6.5	6	5	4		
4. Voltage code								$t_{gt}, \mu s$	1.25	1.60	2.00	2.50		
5. Critical rate of rise of off-state voltage								3) Turn-off time ( $dv_D/dt=50 V/\mu s$ )						
6. Group of turn-off time ( $dv_D/dt=50 V/\mu s$ )								Symbol of group	9.5	9	8.5	8		
7. Group of turn-on time								$t_{qf}, \mu s$	6.30	8.00	10.0	12.5		

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## OVERALL DIMENSIONS

Package type: **T.A1**



All dimensions in millimeters (inches)