

# EVLYS LTD. - POWER SEMICONDUCTORS DEVICES - Wholesale and Retail.

## Fast Thyristor Type **FDT32-250-24**

Low switching losses  
Distributed amplified gate for high  $di_T/dt$

Mean on-state current	$I_{TAV}$	250 A			
Repetitive peak off-state voltage	$V_{DRM}$	1600...2400 V			
Repetitive peak reverse voltage	$V_{RRM}$				
Turn-off time	$t_q$	20.0, 25.0, 32.0, 40.0 $\mu s$			
$V_{DRM}, V_{RRM}, V$	1600	1800	2000	2200	2400
Voltage code	16	18	20	22	24
$T_j, ^\circ C$	-60...+125				

### MAXIMUM ALLOWABLE RATINGS

Symbols and parameters		Units	Values	Test conditions
<b>ON-STATE</b>				
$I_{TAV}$	Mean on-state current	A	250 299 435	$T_c = 94^\circ C$ ; Double side cooled; $T_c = 85^\circ C$ ; Double side cooled; $T_c = 55^\circ C$ ; Double side cooled; 180° half-sine wave; 50 Hz
$I_{TRMS}$	RMS on-state current	A	393	$T_c = 94^\circ C$ ; Double side cooled; 180° half-sine wave; 50 Hz
$I_{TSM}$	Surge on-state current	kA	5.0 6.0	$T_j = T_{jmax}$ $T_j = 25^\circ C$ 180° half-sine wave; $t_p = 10$ ms; single pulse; $V_D = V_R = 0$ V; Gate pulse: $I_G = I_{FGM}$ ; $V_G = 20$ V; $t_{GP} = 50$ $\mu s$ ; $di_G/dt = 1$ A/ $\mu s$
			5.5 6.5	$T_j = T_{jmax}$ $T_j = 25^\circ C$ 180° half-sine wave; $t_p = 8.3$ ms; single pulse; $V_D = V_R = 0$ V; Gate pulse: $I_G = I_{FGM}$ ; $V_G = 20$ V; $t_{GP} = 50$ $\mu s$ ; $di_G/dt = 1$ A/ $\mu s$
$I^2t$	Safety factor	$A^2s \cdot 10^3$	120 180	$T_j = T_{jmax}$ $T_j = 25^\circ C$ 180° half-sine wave; $t_p = 10$ ms; single pulse; $V_D = V_R = 0$ V; Gate pulse: $I_G = I_{FGM}$ ; $V_G = 20$ V; $t_{GP} = 50$ $\mu s$ ; $di_G/dt = 1$ A/ $\mu s$
			120 170	$T_j = T_{jmax}$ $T_j = 25^\circ C$ 180° half-sine wave; $t_p = 8.3$ ms; single pulse; $V_D = V_R = 0$ V; Gate pulse: $I_G = I_{FGM}$ ; $V_G = 20$ V; $t_{GP} = 50$ $\mu s$ ; $di_G/dt = 1$ A/ $\mu s$
<b>BLOCKING</b>				
$V_{DRM}, V_{RRM}$	Repetitive peak off-state and Repetitive peak reverse voltages	V	1600...2400	$T_{jmin} < T_j < T_{jmax}$ ; 180° half-sine wave; 50 Hz; Gate open
$V_{DSM}, V_{RSM}$	Non-repetitive peak off-state and Non-repetitive peak reverse voltages	V	1700...2500	$T_{jmin} < T_j < T_{jmax}$ ; 180° half-sine wave; single pulse; Gate open
$V_D, V_R$	Direct off-state and Direct reverse voltages	V	$0.6 \cdot V_{DRM}$ $0.6 \cdot V_{RRM}$	$T_j = T_{jmax}$ ; Gate open

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<b>TRIGGERING</b>				
$I_{FGM}$	Peak forward gate current	A	6	$T_j = T_{j\max}$
$V_{RGM}$	Peak reverse gate voltage	V	5	
$P_G$	Gate power dissipation	W	3	$T_j = T_{j\max}$ for DC gate current
<b>SWITCHING</b>				
$(di_T/dt)_{crit}$	Critical rate of rise of on-state current non-repetitive (f=1 Hz)	A/ $\mu$ s	1600	$T_j = T_{j\max}$ ; $V_D = 0.67 \cdot V_{DRM}$ ; $I_{TM} = 800$ A; Gate pulse: $I_G = 2$ A; $V_G = 20$ V; $t_{GP} = 50$ $\mu$ s; $di_G/dt = 2$ A/ $\mu$ s
<b>THERMAL</b>				
$T_{stg}$	Storage temperature	$^{\circ}$ C	-60...+50	
$T_j$	Operating junction temperature	$^{\circ}$ C	-60...+125	
<b>MECHANICAL</b>				
F	Mounting force	kN	9.0...11.0	
a	Acceleration	m/s <sup>2</sup>	50	Device clamped

## CHARACTERISTICS

Symbols and parameters		Units	Values	Conditions	
<b>ON-STATE</b>					
$V_{TM}$	Peak on-state voltage, max	V	3.20	$T_j = 25$ $^{\circ}$ C; $I_{TM} = 785$ A	
$V_{T(TO)}$	On-state threshold voltage, max	V	1.841	$T_j = T_{j\max}$ ;	
$r_T$	On-state slope resistance, max	m $\Omega$	2.043	$0.5 \pi I_{TAV} < I_T < 1.5 \pi I_{TAV}$	
$I_H$	Holding current, max	mA	500	$T_j = 25$ $^{\circ}$ C; $V_D = 12$ V; Gate open	
<b>BLOCKING</b>					
$I_{DRM}, I_{RRM}$	Repetitive peak off-state and Repetitive peak reverse currents, max	mA	50	$T_j = T_{j\max}$ ; $V_D = V_{DRM}$ ; $V_R = V_{RRM}$	
$(dv_D/dt)_{crit}$	Critical rate of rise of off-state voltage <sup>1)</sup> , min	V/ $\mu$ s	200, 320, 500, 1000, 1600, 2000, 2500	$T_j = T_{j\max}$ ; $V_D = 0.67 \cdot V_{DRM}$ ; Gate open	
<b>TRIGGERING</b>					
$V_{GT}$	Gate trigger direct voltage, max	V	3.00 2.50 1.50	$T_j = T_{j\min}$ $T_j = 25$ $^{\circ}$ C $T_j = T_{j\max}$	$V_D = 12$ V; $I_D = 3$ A; Direct gate current
$I_{GT}$	Gate trigger direct current, max	mA	500 300 150	$T_j = T_{j\min}$ $T_j = 25$ $^{\circ}$ C $T_j = T_{j\max}$	
$V_{GD}$	Gate non-trigger direct voltage, min	V	0.35	$T_j = T_{j\max}$ ; $V_D = 0.67 \cdot V_{DRM}$ ;	
$I_{GD}$	Gate non-trigger direct current, min	mA	35.00	Direct gate current	
<b>SWITCHING</b>					
$t_{gd}$	Delay time, max	$\mu$ s	0.90	$T_j = 25$ $^{\circ}$ C; $V_D = 1000$ V; $I_{TM} = I_{TAV}$ ; $di/dt = 200$ A/ $\mu$ s;	
$t_{gt}$	Turn-on time <sup>2)</sup> , max	$\mu$ s	2.00, 2.50, 3.20, 4.00	Gate pulse: $I_G = 2$ A; $V_G = 20$ V; $t_{GP} = 50$ $\mu$ s; $di_G/dt = 2$ A/ $\mu$ s	
$t_q$	Turn-off time <sup>3)</sup> max	$\mu$ s	20.0, 25.0, 32.0, 40.0	$dv_D/dt = 50$ V/ $\mu$ s	$T_j = T_{j\max}$ ; $I_{TM} = I_{TAV}$ ; $di_R/dt = -10$ A/ $\mu$ s; $V_R = 100$ V; $V_D = 0.67 V_{DRM}$
			25.0, 32.0, 40.0, 50.0	$dv_D/dt = 200$ V/ $\mu$ s	

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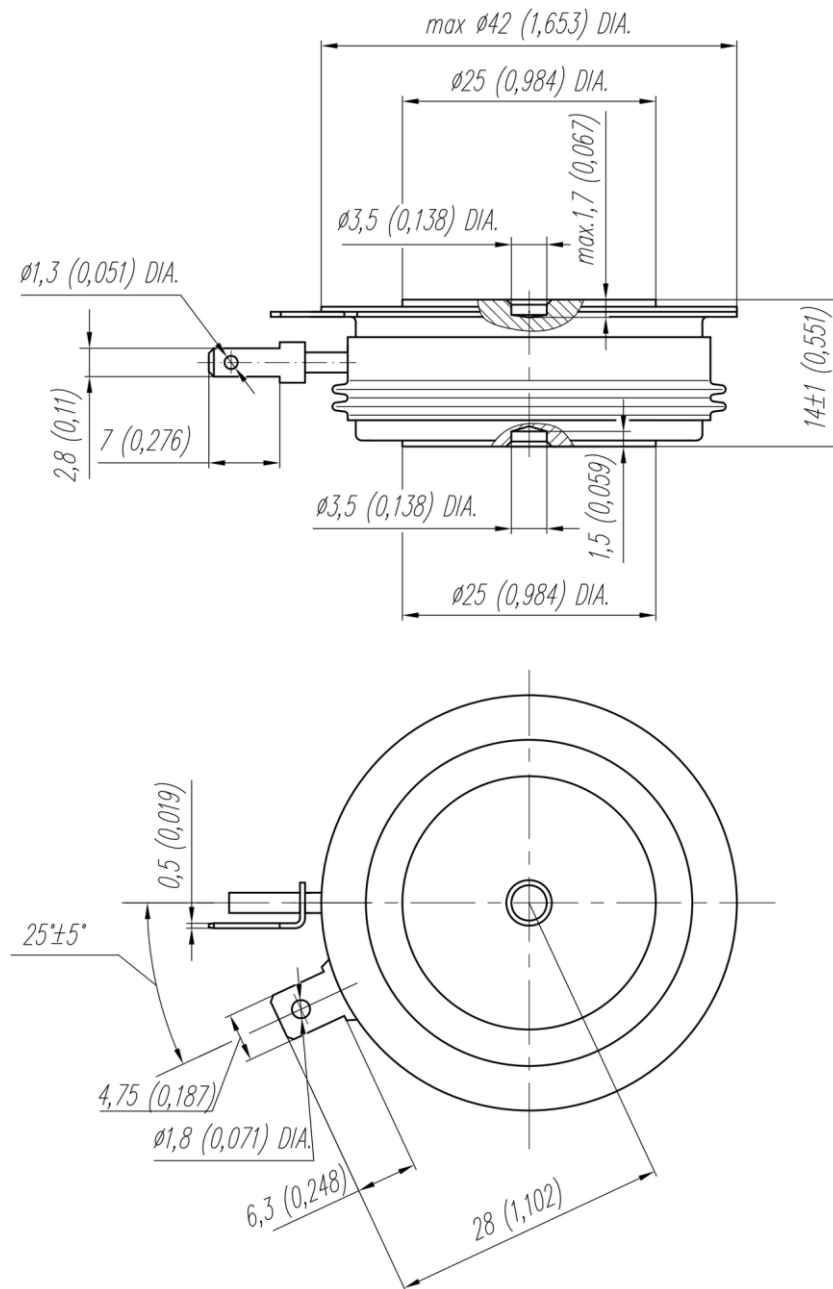
<b>THERMAL</b>					
$R_{thjc}$	Thermal resistance, junction to case, max	°C/W	0.0400	Direct current	Double side cooled
$R_{thjc-A}$			0.0880		Anode side cooled
$R_{thjc-K}$			0.0720		Cathode side cooled
$R_{thck}$	Thermal resistance, case to heatsink, max	°C/W	0.006	Direct current	
<b>MECHANICAL</b>					
w	Weight, max	g	92		
$D_s$	Surface creepage distance	mm (inch)	10.30 (0.405)		
$D_a$	Air strike distance	mm (inch)	6.30 (0.248)		

<b>PART NUMBERING GUIDE</b>								<b>NOTES</b>										
FDT	32	250	24	7	2	6		1) Critical rate of rise of off-state voltage										
1	2	3	4	5	6	7		Symbol of Group		4	5	6	7	8	8,5	9		
1. FDT — Fast Disc Thyristor								$(dv_D/dt)_{crit}, V/\mu s$		200	320	500	1000	1600	2000	2500		
2. Element Diameter								2) Turn-on time										
3. Mean on-state current, A								Symbol of group		5	4	3	2					
4. Voltage code								$t_{gt}, \mu s$		2.00	2.50	3.20	4.00					
5. Critical rate of rise of off-state voltage								3) Turn-off time ( $dv_D/dt=50 V/\mu s$ )										
6. Group of turn-off time ( $dv_D/dt=50 V/\mu s$ )								Symbol of group		6	5	4	3					
7. Group of turn-on time								$t_q, \mu s$		20.0	25.0	32.0	40.0					

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## OVERALL DIMENSIONS

Package type: T.B2



All dimensions in millimeters (inches)

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